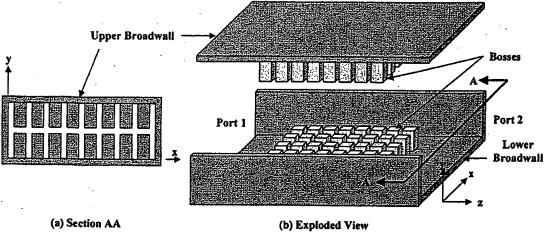
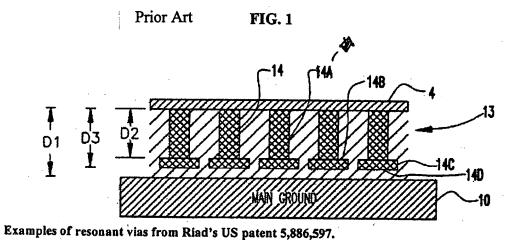


Replacement Drawing Sheet
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TITLE: "SYSTEMS AND METHODS FOR BLOCKING
MICROWAVE PROPATATION IN PARALLEL..."
Application No.: 10/796,398 – Filed: 03/08/2004
Inventor(s): William E. McKinzie, III

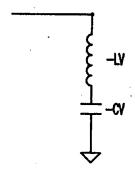
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Waffle-Iron Waveguide Filter: (a) view looking into port 2, (b) exploded view.



Prior Art FIG. 2



Prior Art

FIG. 3

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2/13

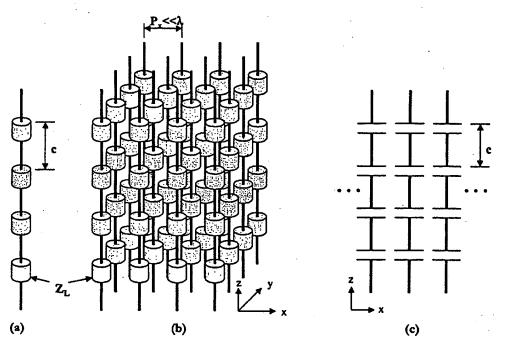
	Mechanically-Unbalanced	Mechanically-Balanced
Internal	Internal "T"	Internal "double T" US Pat. 6,542,342
	US Pat. 5,886,597	Internal "I"
External	External "T" US Pat. 5,886,597	External "I"
Hybrid		Hybrid "I"

Examples of resonant vias. Dielectric layers required for support are not shown.

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Application No.: 10/796,398 - Filed: 03/08/2004 Inventor(s): William E. McKinzie, III

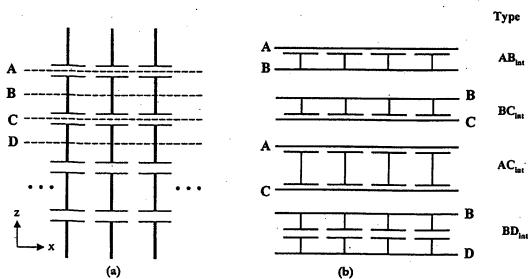
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The starting point for the derivation of new inventions is the loaded wire media: (a) a single wire with uniform periodic series loads, (b) a rectangular array of loaded wires, (c) loads are now defined as parallel-plate capacitors.

Prior Art

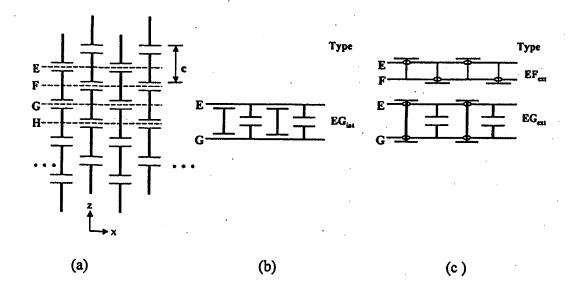
FIG. 5



Exploit the planes of physical symmetry to obtain electromagnetically equivalent structures: (a) the infinite wire media, (b) PPW structures of finite height. The supporting dielectric structure is not shown.

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Alternative embodiments of PPW stop band filters: (a) Capacitive-loaded infinite wire media, (b) a PPW filter of finite height using internal capacitors, (c) PPW filters of finite height using external capacitors. The supporting dielectric structure is not shown.

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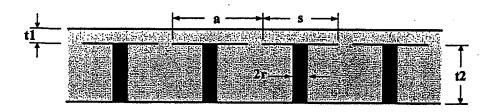
Type
H1
H4

H4

H5
H3
H6

Hybrid embodiments include combined internal and external capacitive loads. Dielectric cores are omitted for clarity.

FIG. 8

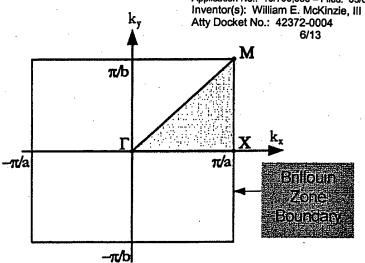


Elevation view of a square lattice of internal T resonant vias.

FIG. 9

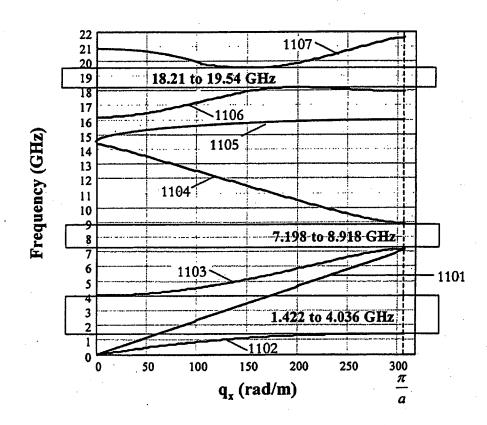
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Brillouin zone for a 2D periodic structure with a rectangular lattice of unit cell dimensions a x b in Cartesian coordinates.

FIG. 10

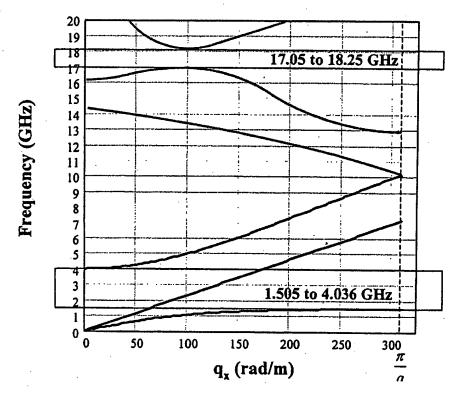


Dispersion diagram along the IX line for the capacitive-loaded wire media corresponding to the array of resonant vias shown in Figure 9. The stop bands are bracketed.

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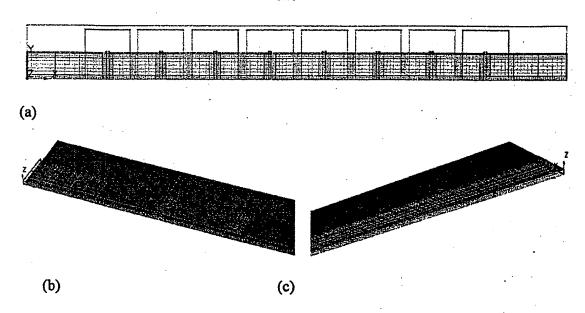
STRUCTURES*

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Dispersion diagram along the ΓM line for the capacitive-loaded wire media corresponding to the array of resonant vias shown in Figure 9. The stop bands are bracketed.

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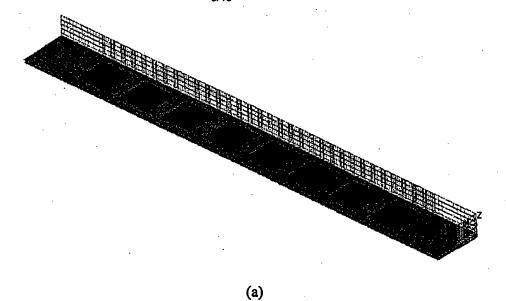


Geometry for the simulation of the resonant via array shown in Figure 9: (a) plan view showing all 8 cascaded unit cells, (b) top view with upper plate hidden, (c) bottom view with the lower plate hidden.

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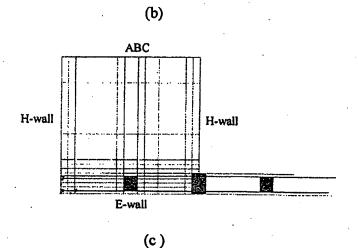
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H-wall

H-wall



Geometry for the simulation of a commensurate period resonant via array of type H12: (a) perspective view showing all 8-cascaded unit cells, (b) plan view, (c) elevation view. Hidden are the dielectric cores in (a), (b) and (c).

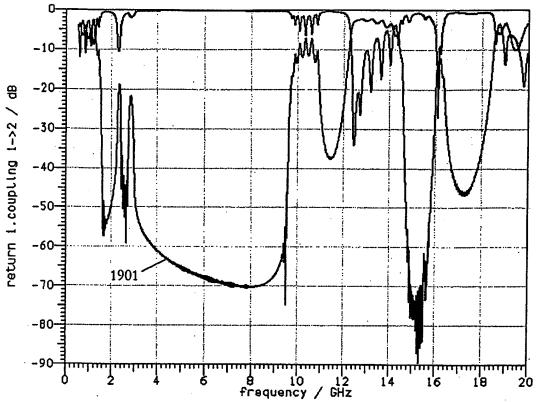
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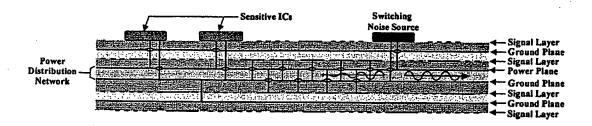
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Lossy T20



Transmission response (1901) for a TEM mode propagating in the x direction through the stop band filter shown in Figure 18. A -10 dB stop band is shown from 1.33 GHz to 10.0 GHz, a 7.5:1 ratio.

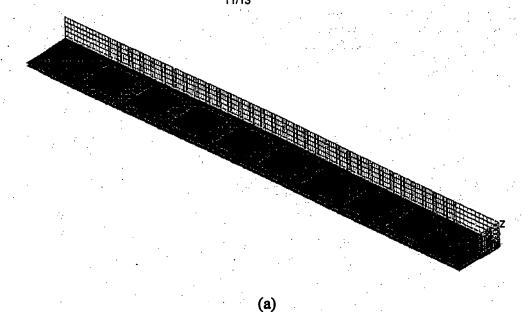
FIG. 19



Stop band filter embodiment EF_{ext} is well suited for integration into existing PWB designs since it requires neither additional metal layers nor any additional thickness.

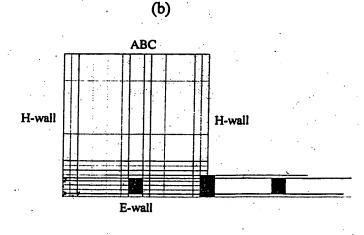
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Application No. 10 (2015)

Application No.: 10/796,398 – Filed: 03/08/2004 Inventor(s): William E. McKinzie, III Atty Docket No.: 42372-0004



H-wall

H-wall



(c)

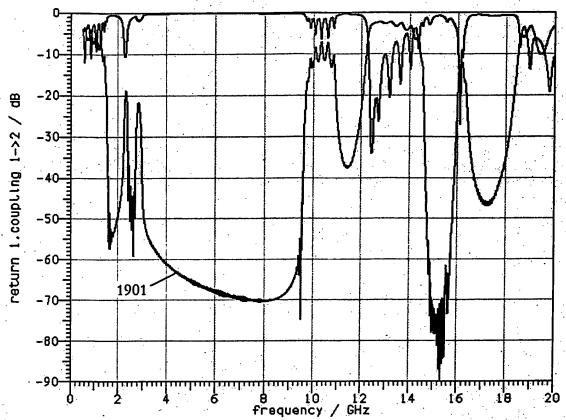
Geometry for the simulation of a commensurate period resonant via array of type H12: (a) perspective view showing all 8-cascaded unit cells, (b) plan view, (c) elevation view. Hidden are the dielectric cores in (a), (b) and (c).

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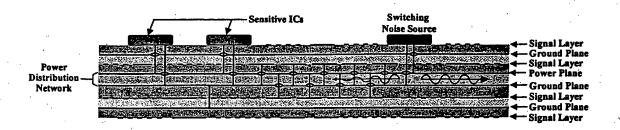
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Lossy T20



Transmission response (1901) for a TEM mode propagating in the x direction through the stop band filter shown in Figure 18. A -10 dB stop band is shown from 1.33 GHz to 10.0 GHz, a 7.5:1 ratio.

FIG. 19

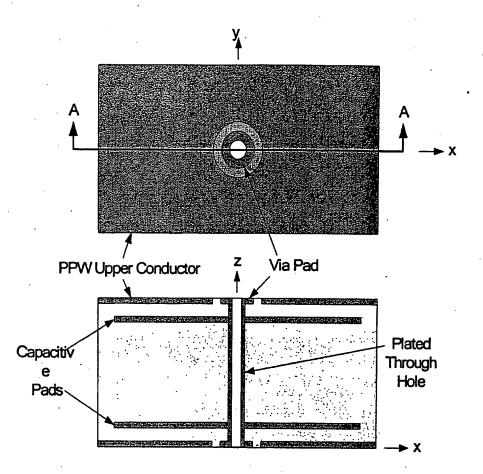


Stop band filter embodiment EF_{ext} is well suited for integration into existing PWB designs since it requires neither additional metal layers nor any additional thickness.

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Plated through holes may be used in the fabrication of resonant vias as illustrated here for an internal I resonant via.

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